CLAIM LISTING

This listing of claims will replace all prior versions, and listings of claims in the application:

AMENDMENTS TO THE CLAIMS

1 – 25 (Canceled)

26. (original) A programmable logic device comprising:

a first voltage supply terminal configured to receive a first supply voltage;

a plurality of programmable logic blocks, each programmable logic block comprising one or more resources of the programmable logic device; and

a plurality of first switch elements, wherein each first switch element is coupled between one of the programmable logic blocks and the first voltage supply terminal.

27. (original) The programmable logic device of Claim 26, further comprising:

a second voltage supply terminal configured to receive a second supply voltage; and

a plurality of second switch elements, wherein each second switch element is coupled between one of the programmable logic blocks and the second voltage supply terminal.

28. (original) The programmable logic device of Claim 26, further comprising a control circuit coupled to the plurality of first switch elements, wherein the control circuit is configured to provide a plurality of control signals for controlling the plurality of first switch elements.

29. (original) The programmable logic device of Claim 28, wherein the control circuit comprises a plurality of configuration memory cells configured to store a corresponding plurality of configuration data values, wherein the control circuit provides the plurality of control signals in response to the plurality of configuration data values.

30. (original) The programmable logic device of Claim 29, wherein the control circuit further comprises a plurality of user control terminals configured to receive a corresponding plurality of user control signals, wherein the control circuit further provides the plurality of control signals in response to the plurality of user control signals.

31. (original) The programmable logic device of Claim 28, wherein the control circuit comprises a plurality of user control terminals configured to receive a corresponding plurality of user control signals, wherein the control circuit provides the plurality of control signals in response to the plurality of user control signals.

32. (original) The programmable logic device of Claim 26, wherein each first switch element comprises a transistor.

33. (original) A programmable logic device comprising:

a first voltage supply terminal configured to receive a first supply voltage;

a plurality of programmable logic blocks, each programmable logic block comprising one or more resources of the programmable logic device; and

a plurality of voltage regulators, wherein each voltage regulator is coupled between one of the programmable logic blocks and the first voltage supply terminal.

34. (original) The programmable logic device of Claim 33, further comprising a control circuit coupled to each of the voltage regulators, wherein the control circuit is configured to provide a plurality of control signals for controlling the plurality of voltage regulators.

35. (original) The programmable logic device of Claim 34, wherein the control circuit comprises a plurality of configuration memory cells configured to store a corresponding plurality of configuration data values, wherein the control circuit provides the plurality of control signals in response to the plurality of configuration data values.

36. (original) The programmable logic device of Claim 35, wherein the control circuit further comprises a plurality of user control terminals configured to receive a corresponding plurality of user control signals, wherein the control circuit further provides the plurality of control signals in response to the plurality of user control signals.

37. (original) The programmable logic device of Claim 34, wherein the control circuit comprises a plurality of user control terminals configured to receive a corresponding plurality of user control signals, wherein the control circuit provides the plurality of control signals in response to the plurality of user control signals.